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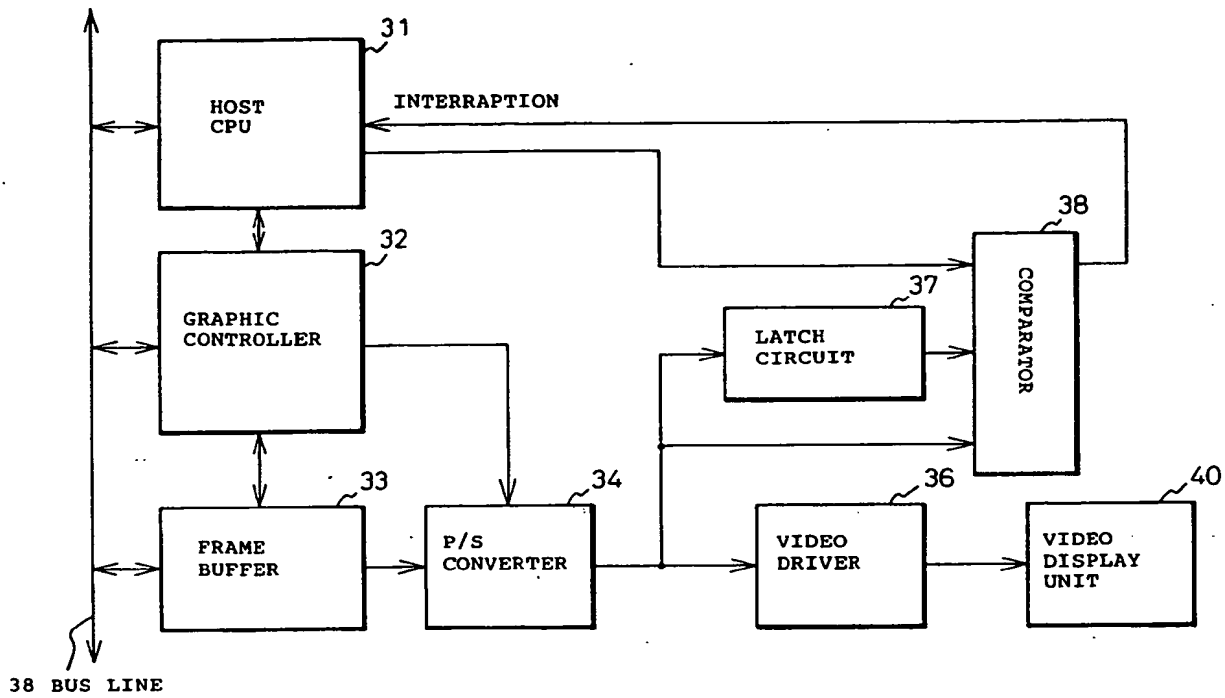
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D-81245 München (DE)⑤④ **GRAPHIC INTERFERENCE CHECK APPARATUS.**

⑤⑦ A graphic interference check apparatus for a machining program which eliminates time and labour by merely generating one screen data. In an NC apparatus including a graphic system which includes a host CPU (31), a graphic controller (32), a frame buffer (33), a P/S convertor (34) for converting a parallel signal to a serial signal, a video driving unit (36) and a video apparatus (40), and wherein the host CPU (31), the graphic controller (32) and the frame buffer (33) are connected by a bus line, the check apparatus of the invention comprises a latch

circuit (37) for latching image data continuously outputted from the P/S convertor (34) for each pixel and a comparison circuit (38) for comparing two adjacent image data between the output of the P/S convertor (34) and the output of the latch circuit (37) with respect to at least two image data set from the host CPU (31) for checking interference, detecting interference and outputting an interrupt signal to the host CPU (31). The host CPU (31) may be provided with the function of the graphic controller (32).

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Fig.4



TECHNICAL FIELD

The present invention relates to CNC machine tools, industrial robots and graphic systems, and more particularly to an apparatus for graphically checking for interference between a workpiece and a tool or the like during machining, in accordance with a machining program, by a CNC machine tool by means of a graphic display.

BACKGROUND ART

At the present time, CNC machine tools with a graphic display include an apparatus for graphically checking for interference between machining cutters that cut a workpiece or between a cutter and a workpiece clamp(chuck), before machining the workpiece in accordance with an NC program, by means of CRT display showing simulation of the machining process of the workpiece as graphical images so that operators can find the interference.

This apparatus for graphically checking for interference between a workpiece and a tool, displays workpiece shapes before and after machining the workpiece, chuck shapes, tail stock shapes, tool shapes on cutting tables or the like on a CRT screen, and displays a tool path in which the tool moves in accordance with an NC program on the CRT screen, and allows to monitor the interference. An example of this apparatus will be explained below.

Fig. 1 is a drawing showing the structure of an apparatus for graphically checking for interference between a workpiece and a tool of a machining program in a prior art. This apparatus includes a host computer 1 controlling the general operations of a numerical controller, a graphic controller 2, connected to the host computer 1, which controls screen information having a plurality of graphic images, a NO.1 frame buffer 3, connected to the graphic controller 2, which stores screen information including a graphic image of a body (any part of a workpiece or a tool) for machining in accordance with a machining program, a NO.2 frame buffer 13, connected to the graphic controller 2, which stores other screen information including a graphic image of other body for machining in accordance with the machining program, a NO.1 P/S converter 4 that receives parallel signals from the NO.1 frame buffer 3, converts the parallel signals into serial signals, and outputs the serial signals in accordance with a command signal from the graphic controller 2, a NO.2 P/S converter 14 that receives parallel signals from the NO.2 frame buffer 13, converts the parallel signals into serial signals, and outputs the serial signals in accordance with another command signal from the graphic controller 2. The apparatus also includes a comparator 8

that receives outputs from the NO.1 P/S converter 4 and from the NO.2 P/S converter 14, compares the outputs continually, and outputs an interruption signal to the host CPU 1 when it detects interference.

The comparator 8 detects interference whether or not the pixel data in the comparator 8, previously set by the host computer 1, indicates two different bodies for interference checking, are input at the same time. When it is determined that the pixel data are input at the same time, then it determines that there is interference.

A video driver 6 outputs a video signal, as screen information, to the video display unit 10, on which the graphic images are displayed, after combining the output from the first P/S converter 4 with the output from the second P/S converter 14.

Furthermore, the host CPU 1, the graphic controller 2, the NO.1 frame buffer 3, and the NO.2 frame buffer 13 are all connected to a bus line 9.

Fig. 2 is a sample drawing showing graphic images displayed on a screen in the apparatus shown in Fig.1. In Fig. 2, one screen information of a graphic image of a body to be displayed in red, stored in NO.1 frame buffer 3, is indicated as a graphic image. And another screen information of a graphic image of another body to be displayed in blue, stored in NO.2 frame buffer 13, is indicated as another graphic image. Screen obtained by combining the screen information stored in NO.1 frame buffer 3 and the screen information stored in NO.2 frame buffer 13, is displayed on the video display unit 10 as a graphic image, indicating the place where the two bodies are overlap in purple.

The apparatus for graphically checking for interference between a workpiece and a tool in accordance with a machining program in a prior art, as can be seen in Fig. 2, is provided with screen information for as many of bodies between which interference will be checked. It is also provided with frame buffers for the screen information that stores the same, and a video display unit that displays screen information after combining screen information for each body stored in the each frame buffer. It checks for interference between the bodies by means of a comparator that detects the overlap of graphic images of bodies in the screen information. This apparatus for graphically checking for interference between a workpiece and a tool detects the interference when pixel information concurrently transmitted from NO.1 frame buffer 3 and NO. 2 frame buffer 13 are different. Namely, in the example shown in Fig. 2, the apparatus for graphically checking for interference between a workpiece and a tool detects interference when the red pixel data output from NO.1 frame buffer 3 and the blue pixel data output from NO.2 frame buffer 13 overlap.

However, the apparatus for graphically checking for interference between a workpiece and a tool in a prior art has to be provided with as many frame buffers as there are bodies between which interference will be checked, and has to store the screen information of each body into each frame buffer. Therefore, there are problems that the size of the capacity of the frame buffers is increased, and that a long time and considerable manual labor are required for making the screen information, because operators have to prepare same number of screen information regarding parts, such as a workpiece and tools for checking for interference.

DISCLOSURE OF THE INVENTION

Therefore, in the light of the above mentioned problems, it is an object of the present invention to provide an apparatus for graphically checking for interference between a workpiece and a tool in accordance with a machining program that has a single frame buffer storing screen information of a plurality of graphic images of bodies thereby saving time and labor for making the screen information.

Fig. 3 is a drawing showing the basic structure of an apparatus for graphically checking for interference between a workpiece and a tool according to the present invention.

In order to solve the above mentioned problems, in the apparatus for graphically checking for interference between a workpiece and a tool according to the present invention includes a host CPU 31, a screen information output circuit 35, a latch 37 and a comparator 38. The host CPU 31 previously sets more than two different pixel data (color information) for checking for the interference, namely, a plurality of kinds of determined length pixel data for graphic display, in a comparator 38. The screen information output circuit 35 is connected with the host CPU 31, stores screen information consisting of a plurality of pixel data in a frame memory in itself, and continually outputs the stored screen information. The latch 37 receives pixel data (color information) continually output from the screen information output circuit 35, latches and updates each pixel data. The comparator 38 compares current pixel data received from the screen information circuit 35 with pixel data received from the latch 37 which is previous to the current pixel data, namely, compares two adjacent pixel data (color information), then detects interference and outputs a signal to the host CPU 31 when two kinds of the pixel data (color information) for checking interference that are previously set by the host CPU 31, are adjacent.

The apparatus for graphically checking interference according to the present invention includes

the means, namely the latch circuit 37, for latching and updating the pixel data of screen information continually output from the screen information output circuit 35, the means, namely the comparator 38, for comparing current pixel data output from the screen information output circuit 35 with the pixel data output from the latch circuit 37 which are previous to the current pixel data individually, in which more than two kinds of pixel data which will be checked for interference are previously set by the host CPU 31, for detecting interference when two different pixel data are adjacent, and for outputting a signal to the host CPU 31.

BRIEF DESCRIPTION OF DRAWINGS

Fig. 1 is a drawing showing the structure of an apparatus for graphically checking for interference between a workpiece and a tool of a machining program in a prior art;

Fig. 2 is a drawing showing graphic images displayed on a screen in the apparatus shown in Fig. 1;

Fig. 3 is a drawing showing the basic structure of an apparatus for graphically checking for interference between a workpiece and a tool according to the present invention;

Fig. 4 is a drawing showing the structure of an embodiment of an apparatus for graphically checking for interference between a workpiece and a tool of a machining program according to the present invention;

Fig. 5A shows a graphic display when two bodies to be checked are separate;

Fig. 5B shows a graphic display when the two bodies are adjacent; and,

Fig. 5C a graphic display when the two bodies are overlapped.

BEST MODE FOR CARRYING OUT THE INVENTION

Fig. 4 is a drawing showing the structure of an embodiment of an apparatus for graphically checking for interference between a workpiece and a tool of a machining program according to the present invention. In Fig. 4, reference numerals the same as those in Fig. 3 indicate the same elements. Explanation of Fig. 4 will now be described hereinafter. The screen information output circuit 35 in Fig. 3 is replaced by a graphic controller 32, a frame buffer 33 and a P/S converter 34 in Fig. 4. The host CPU 31 controls the general operations of the numerical controller, is connected to the graphic controller 32 and determines the screen information after defining the shapes of workpieces, chucks and cutting tools. Furthermore, the host CPU 31 can produce more than two body color

information sets on which interference will be checked, namely, any two or more combinations of body colors from red, green and blue. The graphic controller 32 operates to memorize screen information, determined by the host CPU 31, in the frame buffer 33 via the bus 39.

The screen information stored in the frame buffer 33 are output as parallel signals to the P/S converter 34.

The P/S converter 34 outputs serial signals after converting the parallel signals to the serial signals in accordance with the command signal from the graphic controller 32. A video driver 36 receives the serial signals from the P/S converter 34 and outputs three color (R,G,B) video signals to a video display unit 40.

The video display unit 40 displays the screen information output from the video driver 36. The latch circuit 37 latches and updates the pixel data which are continually output from the P/S converter 34 and which are stored as the screen information in the frame buffer 33. In this embodiment, three bits of color information (R,G,B) per a pixel are output from the P/S converter 34. The comparator 38 compares the output from the P/S converter 34 with the output from the latch circuit 37 and outputs a signal to the host CPU 31 when interference is detected.

To detect interference, more than two body color information sets for checking interference are previously produced by the host CPU 31. Namely, at least two color information out of R, G, B are previously selected and are stored in the comparator 38 by the host CPU 31. Interference is detected when the different bodies are adjacent, i. e., different kinds of color information are detected at adjacent pixel data.

If the signal from the comparator 38 is used as an interruption signal, then the actual interference position, where different colors are adjacent, can be observed by stopping the screen when the interference is detected, and the machining NC program can be properly corrected since actual interference positions can be confirmed by data analysis.

Furthermore, the apparatus for graphically checking for interference between a workpiece and a tool according to the present invention can also be realized without having a graphical controller 32, but by having a host CPU 31 with the functions of a graphical controller 32 as shown in Fig.4.

Fig. 5 indicates three mode of two bodies between which the interference will be checked. The mode of operation of the apparatus shown in Fig. 4 will be explained hereinafter with reference to the Fig. 5.

Fig. 5A shows a graphic display when two bodies for checking the interference are separate,

Fig. 5B shows a graphic display when the two bodies are adjacent, and Fig. 5C a graphic display when the two bodies are overlapped.

A body 51 is indicated by red, while a body 52 is indicated by blue. The host CPU 31 previously set red and blue in the comparator 38. Corresponding to the output from the P/S converter 34, Fig. 5A indicates that red and blue pixels are in separated positions, Fig. 5B indicates that red and blue pixels are in adjacent positions, and Fig. 5C indicates that red and blue pixels are overlapped on the screen respectively. In other words, corresponding to the output from the P/S converter 34, Fig. 5A indicates red and blue pixels are output in a different time, Fig. 5B indicates blue pixels are output just adjacent to red pixels, and Fig. 5C indicates red pixels are independently output at first, then red and blue pixels are output simultaneously, and blue pixels are independently output at the end. The latch 37 outputs pixel information that are one pixel delayed compared to the pixel information output from the P/S converter 34. The comparator 38 outputs an alarm signal to the host CPU 31 in the cases of Fig. 5B and Fig.5C after detecting that two adjacent pixel information sets are different, because two adjacent pixel information sets are red and blue in these cases, and they are previously set by the host CPU 31 as the color information for checking interference.

Other methods for checking for interference in a machining program by software other than the apparatus according to the present invention can be considered. Namely, one method may include the steps of copying image screen information by combining two respective bodies for checking interference; then comparing the screen information before copying, namely, the information of each body, with the screen information after copying, namely, that combined information; and detecting the interference between the two bodies. Another method may include the steps of comparing the screen information of each body with the screen information that combines information of the two respective bodies for checking interference, before the copying; and detecting the interference.

According to these software methods, the host CPU represents (copies) the cutter (cutting tool) shape previously defined on the frame buffer, and continually copies the cutter shape along the cutter moving route, then operators can observe by naked eyes as if the cutter is moving, since, for example, the course where the cutter, as a body, moves is known by the host CPU.

When the colors are set such as red for the cutter, blue for a chuck, and purple for the interference portion between the cutter and the chuck, the host CPU detects the interference in the course of continual coping, by detecting the fact that red

interfered with blue and that the interference portion changed to purple in the frame buffer, and the host CPU sets the alarm bit via the software.

In this case, each bit data in some square area is checked to detect the interference. To accomplish this process by software, a long processing time is required, the scan time of the host computer that executes the software is longer than that of video signal for the CRT screen, which means that processing time of the software requires more time than hardware processing time. Furthermore, the software process is complicated and it requires time, therefore it is not suitable to realize the apparatus for detecting the interference although there is a merit that graphic image information of a plurality of bodies can be stored in a frame buffer as one screen information, which is the same merit of the present invention. Therefore, the apparatus for detecting interference according to the present invention may be the best of all the apparatus which use current electronic equipment, electronic elements or the like.

As heretofore explained, according to the apparatus for graphically checking interference between a workpiece and a tool of the present invention, it is not necessary to provide the same number of screen information sets as the number of bodies for checking interference. The apparatus only requires one screen information set for a plurality of kinds of bodies, so that time and labor can be saved for preparation and a quick and effective interference check can be realized.

Furthermore, according to the apparatus for graphically checking interference of the present invention, by transmitting an interruption signal to the CPU after detecting the interference, the video screen can be stopped to observe the standstill state of the interference and different colors in adjacent positions can be actually observed and the interference position can be confirmed. And accurate feedback for correcting the machining program can be accomplished since the actual interference position can be confirmed by data analysis.

Claims

1. An apparatus for graphically checking interference between a workpiece and a tool which has a plurality of kinds of predetermined lengths of graphic display pixel data and can determine that interference occurred between said kinds of pixel data when data at one or more addresses in said predetermined length of graphic display pixel data in said more than two kinds of pixel data, are the same, is characterized in that it comprises:

a host CPU (31) which previously set said

kinds of pixel data in a comparator (38) for checking said interference;

a screen information output circuit (35) connected to said host CPU (31) which outputs said each pixel data from screen information having said pixel data;

a latch circuit (37) which receives said pixel data continually output from said screen information output circuit (35), and latches and updates said pixel data;

a comparator (38) that receives said pixel data output from said screen information output circuit (35) and pixel data that are the previous pixel data to said current pixel data that are output from said latch (37), compares between them (compares said two adjacent pixel data) determines said interference when said kinds of two pixel data previously set for checking interference by said host CPU (31) are adjacent, and outputs a signal to said host CPU (31).

2. An apparatus for graphically checking interference between a workpiece and a tool according to a numerical controller having a graphic system comprising,

a host CPU (31) for controlling the operations of said numerical controller,

a graphic controller (32) connected to said host CPU (31),

a frame buffer (33) storing screen information having a plurality of graphic images of bodies,

a P/S converter (34) that receives parallel signals output from said frame buffer (33) and outputs serial signals in accordance with command signals from said graphic controller (32) after converting from said parallel signals to serial signals,

a video driver (36) that receives video signals from said P/S converter (34) and outputs said video signals, and

a video display unit (40) that displays a graphic image after receiving said video signals from said video driver (36) and in that a bus line is connected to said host CPU (31), said graphic controller (32) and said frame buffer (33), or another apparatus which does not include said host CPU (31) but includes said graphic controller (32) having the function of said host CPU (31), is characterized in that it comprises,

a latch circuit (37) that receives said screen information having pixel data continually output from said P/S converter (34), latches and updates said pixel data, and

a comparator (38) that receives said pixel data currently output from said P/S converter

(34) and pixel data output from said latch circuit (37) in which pixel data are output one pixel earlier than said current pixel data, compares them, namely, compares two adjacent pixel data, determines an interference when two pixel data representing two bodies for checking interference, which are previously set by said host CPU (31), are adjacent, namely, determines said interference when two different bodies are adjacent, and outputs a signal to said host CPU (31).

3. An apparatus claimed in claim 1 or 2, wherein said comparator outputs an interrupting signal to said host CPU (31) after detecting said interference.

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Fig.1

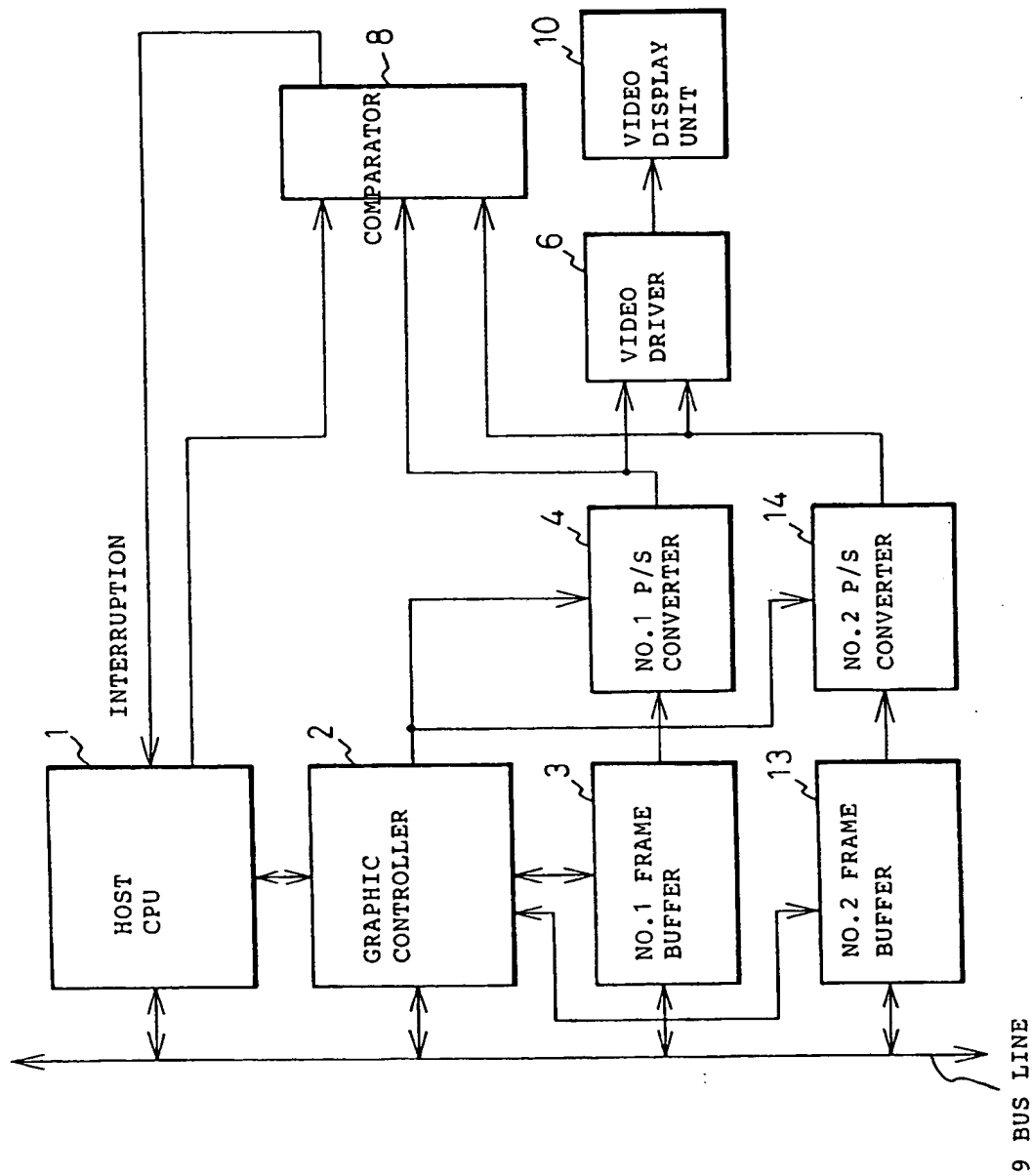


Fig.2

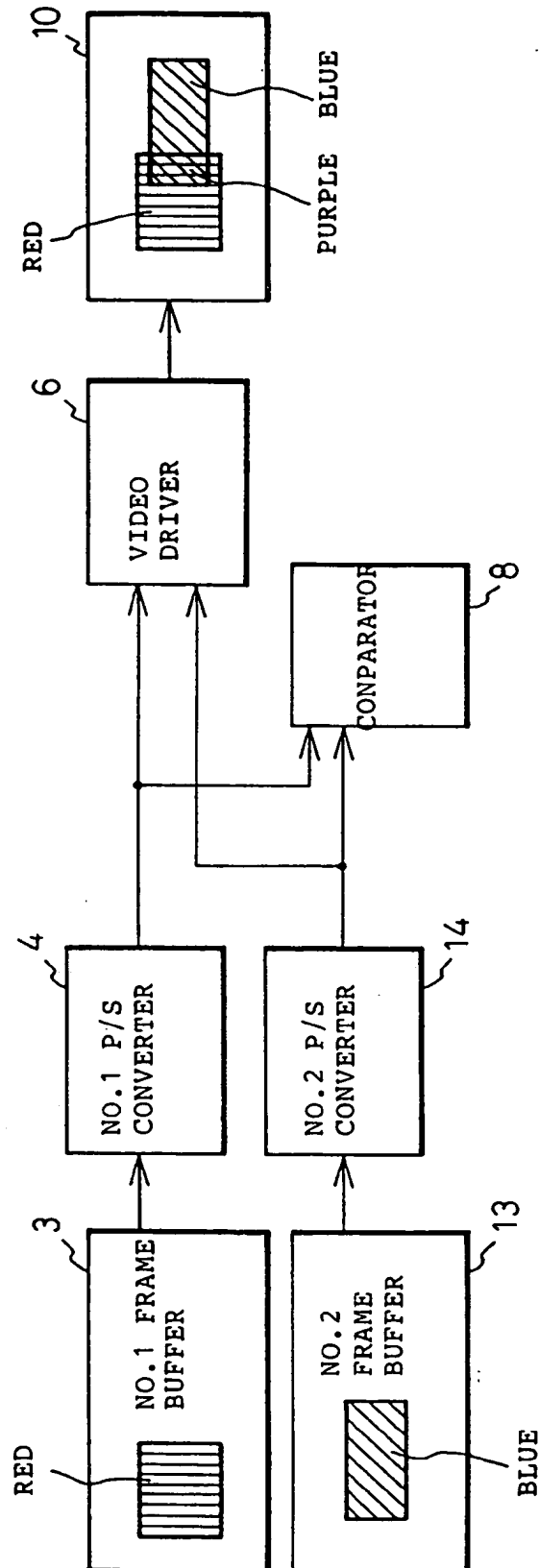


Fig.3

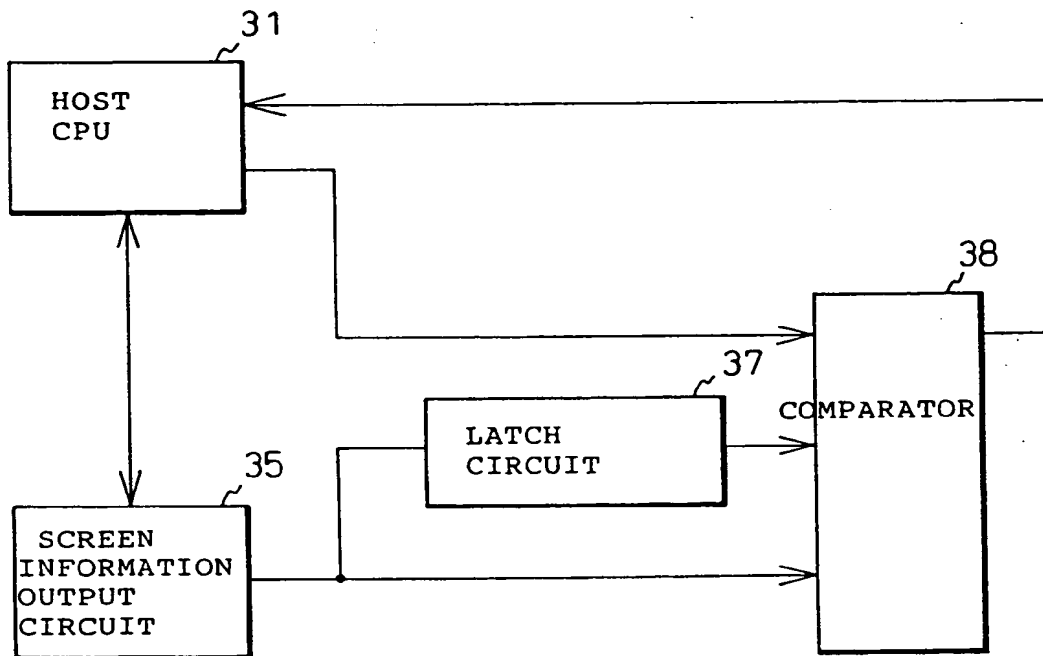


Fig.4

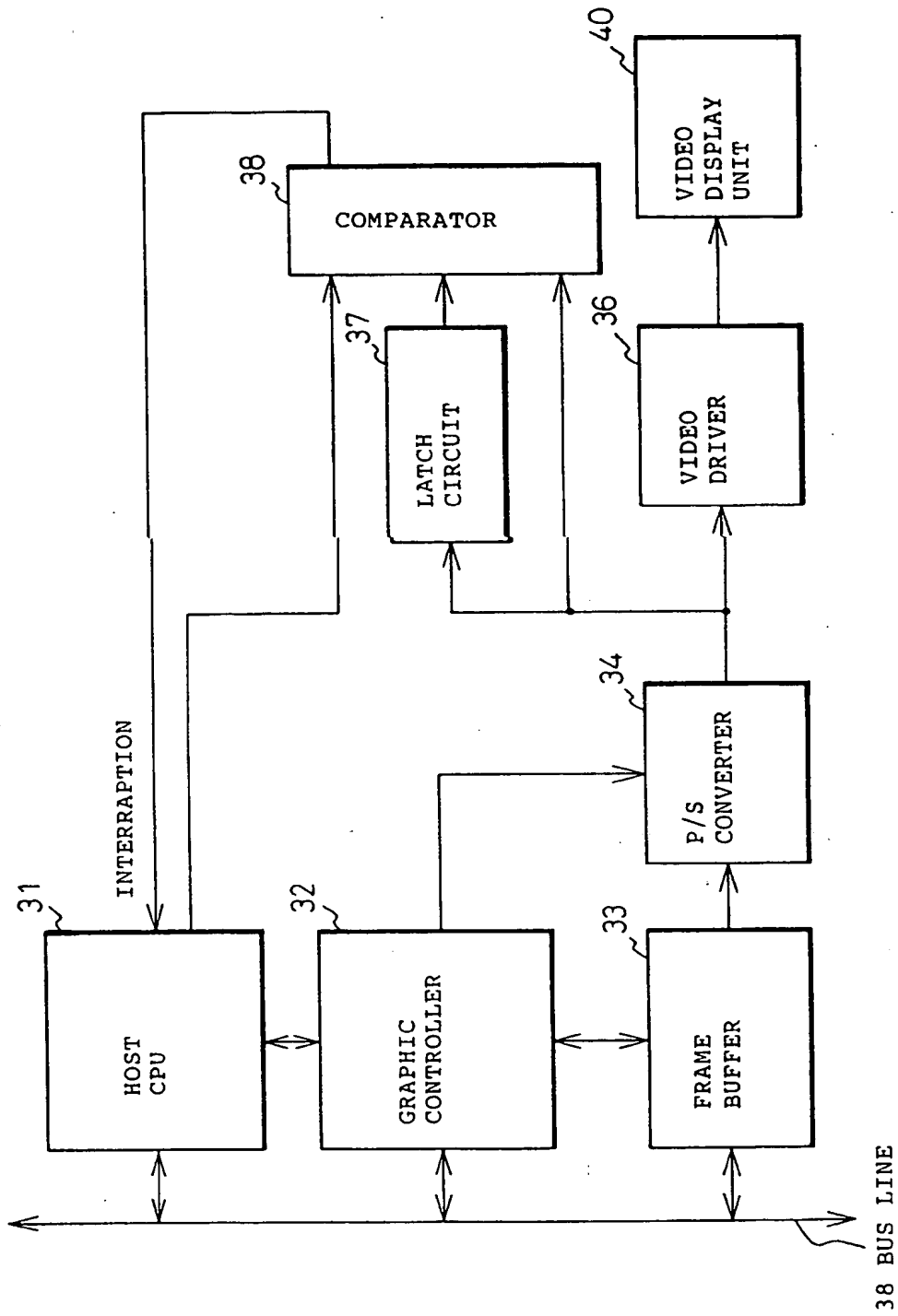


Fig.5A

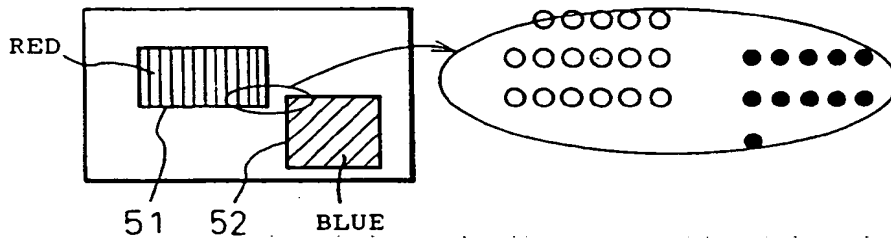


Fig.5B

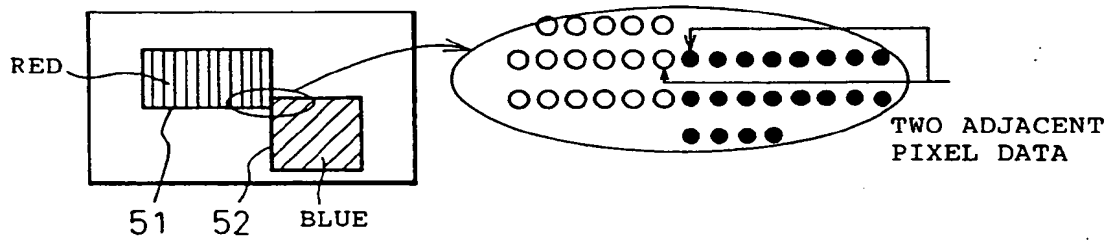
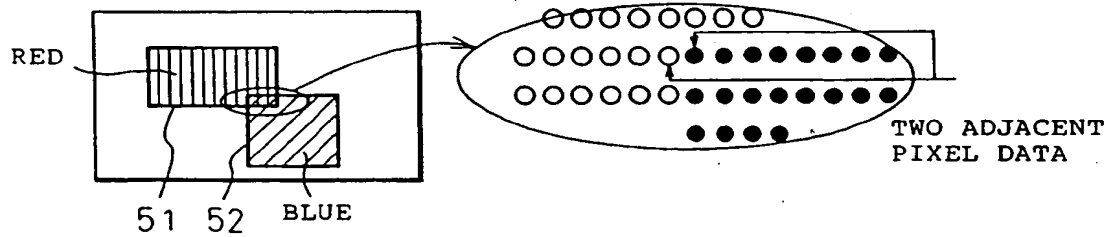


Fig.5C



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP93/00098

A. CLASSIFICATION OF SUBJECT MATTER Int. Cl ⁵ G05B19/405 According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int. Cl ⁵ G05B19/405 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926 - 1993 Kokai Jitsuyo Shinan Koho 1971 - 1993 Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP, A, 63-64107 (Toshiba Machine Co., Ltd.), March 22, 1988 (22. 03. 88), Line 4, upper left column to line 17, upper right column, page 3 (Family: none)	1-3
Y	JP, A, 62-136340 (Fanuc Ltd.), June 19, 1987 (19. 06. 87), Line 14, upper right column, page 3 to line 16, upper left column, page 4 & WO, A1, 8703524 & EP, A1, 250597 & US, A, 4821201 & KR, B1, 9107257	1-3
Y	JP, A, 63-207537 (Hitachi Seiki Co., Ltd.), August 26, 1988 (26. 08. 88), Line 19, lower left column, page 3 to line 3, upper left column, page 4 (Family: none)	1-3
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance: the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance: the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family		
Date of the actual completion of the international search April 15, 1993 (15. 04. 93)		Date of mailing of the international search report May 11, 1993 (11. 05. 93)
Name and mailing address of the ISA/ Japanese Patent Office Facsimile No.		Authorized officer Telephone No.

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